## **GENERAL DESCRIPTION**

The LM104, LM204, and LM304 are precision voltage regulators which provide programmable output voltage levels. With an external resistor the voltage levels can be programmed from 40V down to 0V.

These devices are primarily designed for systems requiring regulated negative voltages which have a common ground with the unregulated supply. They are complements of the LM105/LM305 positive regulators.

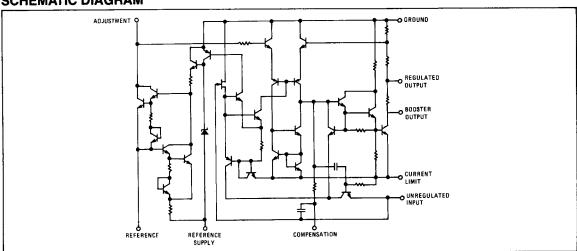
The LM104 series can also be used as switching or current regulators, or in a variety of control applications. Both constant and foldback current limiting are available.

The LM104 operates over the military temperature range of -55°C to +125°C. The LM304 is the commercial version which operates from 0°C to +70°C. The LM204 is the same as the LM104 except its performance is guaranteed from -25°C to +85°C.

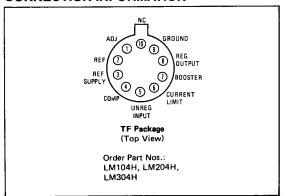
### **DESIGN FEATURES**

- No-Load to Full-Load Regulation 1mV
- Line Regulation 0.01%/V
- Ripple Rejection 0.2mV/V
- Temperature Stability 0.3% from -55°C to +125°C

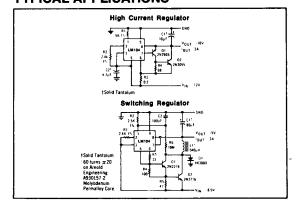
### SCHEMATIC DIAGRAM



### **CONNECTION INFORMATION**



## TYPICAL APPLICATIONS





# 3

### **ABSOLUTE MAXIMUM RATINGS**

Input Voltage LM104/LM204: 50V	Operating Temperature Range
LM304: 40V	LM104
Input-Output Voltage Differential LM104/LM204: 50V	LM20425°C to +85°C
LM304: 40V	LM304 0°C to +70°C
Power Dissipation (Note 1) 500mW	Storage Temperature Range65°C to +150°C
	Lead Temperature (Soldering, 10s) 300°C

#### **ELECTRICAL CHARACTERISTICS** (Note 2)

PARAMETER	CONDITIONS	LM104/LM204			LM304			
		MIN	TYP	MAX	MIN	TYP	MAX	UNITS
Input Voltage Range		-50		-8	-40		-8	V
Output Voltage Range		-40		-0.015	-30		-0.035	V
Output-Input Voltage Differential (Note 3)	I <sub>O</sub> = 20mA	2.0		50	2.0		40	V
	I <sub>O</sub> = 5 mA	0.5		50	0.5		40	V
Load Regulation (Note 4)	$0 \le I_O \le 20$ mA R <sub>SC</sub> = 15 $\Omega$		1	5		1	5	mV
Line Regulation (Note 5)	$V_{OUT} \le -5V$ $\Delta V_{IN} = 0.1 V_{IN}$		0.056	0.1		0.056	0.1	%
Ripple Rejection	$C_{19} = 10\mu\text{F}, f = 120\text{Hz}$ $V_{1N} < -15\text{V}$		0.2	0.5		0.2	0.5	mV/V
	-7V ≥ V <sub>IN</sub> ≥ -15V		0.5	1.0		0.5	1.0	mV/V
Output Voltage Scale Factor	R <sub>23</sub> = 2.4k	1.8	2.0	2.2	1.8	2.0	2.2	V/kΩ
Temperature Stability	V <sub>0</sub> ≤ −1V		0.3	1.0				%
	V <sub>O</sub> ≤-1V,0°C ≤ T <sub>A</sub> ≤ 70°C					0.3	1.0	
Output Noise Voltage	10Hz ≤ f ≤ 10kHz $V_0$ ≤ -5V, $C_{19}$ = 0		0.007			0.007		%
	C <sub>19</sub> = 10μF		15			15		μV
Standby Current Drain	$I_L = 5mA, V_O = 0$		1.7	2.5		1.7	2.5	mA
	V <sub>O</sub> = -40V		3.6	5.0				mA
	V <sub>O</sub> = -30V					3.6	5.0	
Long Term Stability	V <sub>0</sub> ≤ −1V		0.1	1.0		0.1	1.0	%

#### NOTES

- 1. The maximum junction temperature of the LM104 is 150°C and the LM304 +85°C. For operating at elevated temperatures, devices in the T0-5 package must be derated based on a thermal resistance of +150°C/W, junction to ambient, or +45°C, junction to case. For the flat package, the derating is based on a thermal resistance of +185°C/W when mounted on a 1/16-inch-thick epoxy glass board with ten, 0.03-inch-wide, 2-ounce copper conductors.
- 2. These specifications apply for junction temperatures between -55°C and +150°C (between 0°C and +85°C for the LM304) and for input and output voltages within the ranges given, unless otherwise specified. The load and line regulation specifications are for constant junction temperature. Temperature drift effects must be taken into
- account separately when the unit is operating under conditions of high dissipation.
- When external booster transistors are used, the minimum outputinput voltage differential is increased, in the worst case, by approximately 1V.
- 4. The output currents given, as well as the load regulation, can be increased by the addition of external transistors. The improvement factor will be roughly equal to the composite current gain of the added transistors.
- 5. With zero output, the dc line regulation is determined from the ripple rejection. Hence, with output voltages between 0V and -5V, a dc output variation, determined from the ripple rejection, must be added to find the worst case line regulation.