FEATURES

* 0.39 inch (10.0 mm) DIGIT HEIGHT * CONTINUOUS UNIFORM SEGMENTS * LOW POWER REQUIREMENT * EXCELLENT CHARACTERS APPEARANCE * HIGH BRIGHTNESS & HIGH CONTRAST * WIDE VIEWING ANGLE * SOLID STATE RELIABILITY * CATEGORIZED FOR LUMINOUS INTENSITY

DESCRIPTION

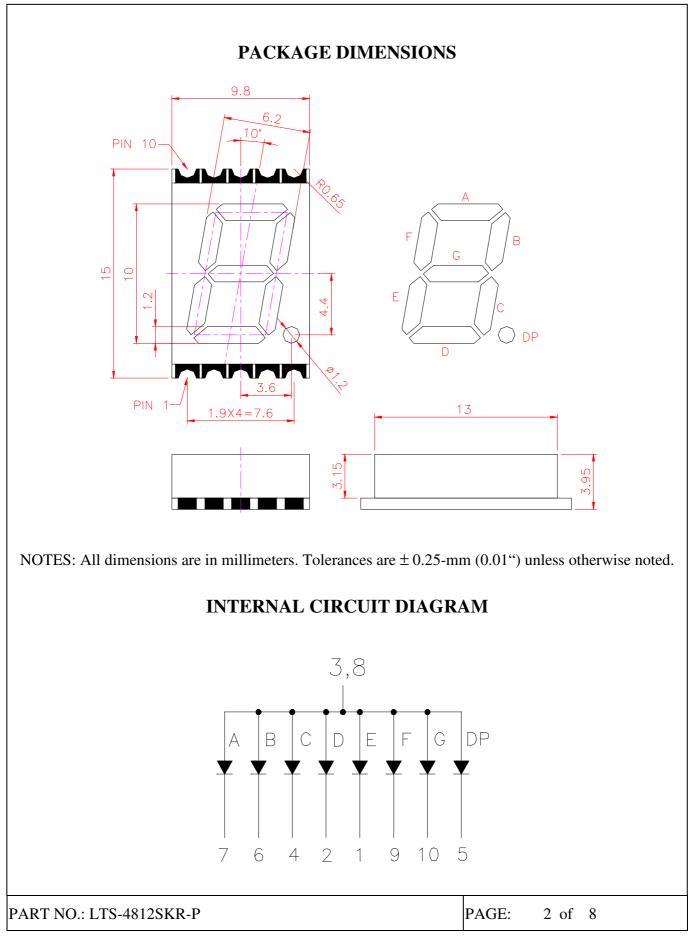
The LTS-4812SKR-P is a 0.39 inch (10.0 mm) digit height single digit SMD display. This device utilizes AllnGaP SUPER RED LED chips which are made from Aluminum Indium Gallium Phosphide on GaAs , The display has gray face and white segments.

DEVICE

PART NO.	DESCRIPTION
AllnGaP SUPER RED	Common Anode
LTS-4812SKR-P	

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PIN CONNECTION

No.	CONNECTION
1	CATHODE E
2	CATHODE D
3	COMMON ANODE
4	CATHODE C
5	CATHODE D.P.
6	CATHODE B
7	CATHODE A
8	COMMON ANODE
9	CATHODE F
10	CATHODE G

PART NO.: LTS-4812SKR-P

ABSOLUTE MAXIMUM RATING AT Ta = 25° C

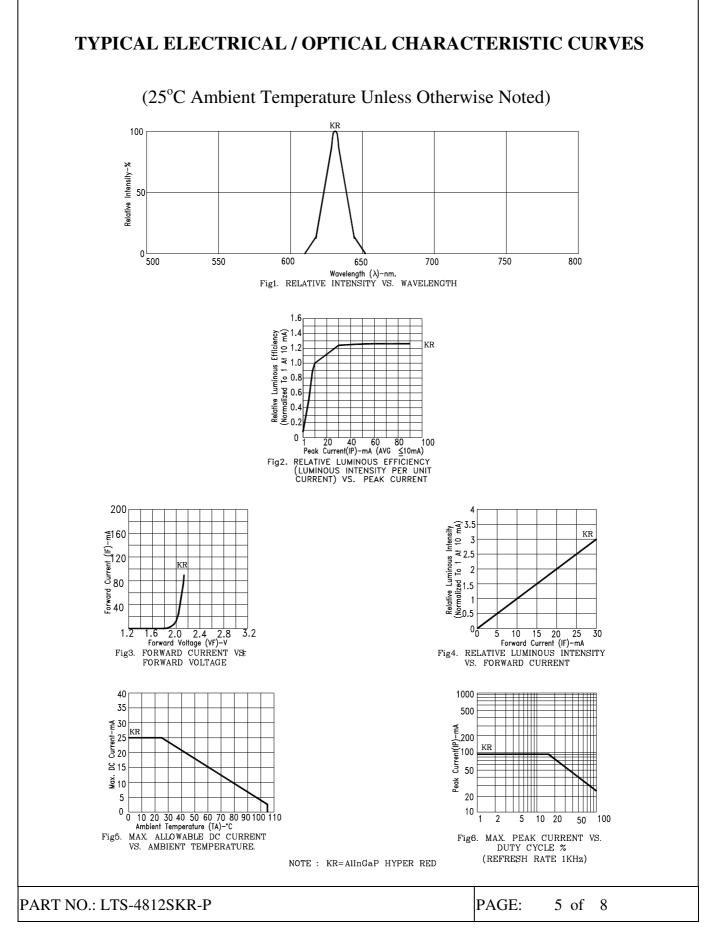
PARAMETER	MAXIMUM RATING	UNIT
Power Dissipation Per Segment	75	mW
Peak Forward Current Per Segment (Frequency 1Khz,10% duty cycle)	90	mA
Continuous Forward Current Per Segment	2.6	mA
Forword Current Derating from 25 ^o C	0.28	mA/ ⁰ C
Reverse Voltage Per Segment	5	V
Operating Temperature Range	-25° C to $+105^{\circ}$ C	
Storage Temperature Range	-25° C to $+105^{\circ}$ C	
Soldering Comditions: 1/16 inch Below Seating	g Plane for 3 Seconds at 260 ⁰ C	

ELECTRICAL / OPTICAL CHARACTERISTICS AT Ta = 25°C

PARAMETER	SYMBOL	MIN.	TYP.	MAX.	UNIT	TEST CONDITION
Average Luminous Intensity Per Segment	Iv	17.5	37.8		mcd	IF=20mA
Peak Emission Wavelength	λp		639		Nm	IF=20mA
Spectral Line Half-Width	Δλ		20		Nm	IF=20mA
Dominant Wavelength	λd		631		Nm	IF=20mA
Forward Voltage Per Segment	VF		2.0	2.4	V	IF=20mA
Reverse Current Per Segment	Ir			100	μA	V _R =5V
Luminous Intensity Matching Ratio	Iv-m			2:1		IF=20mA

Note:Luminous intensity is measured with a light sensor and filter combination that approximates the CIE (Commision Internationale De L'Eclairage) eye-response curve.

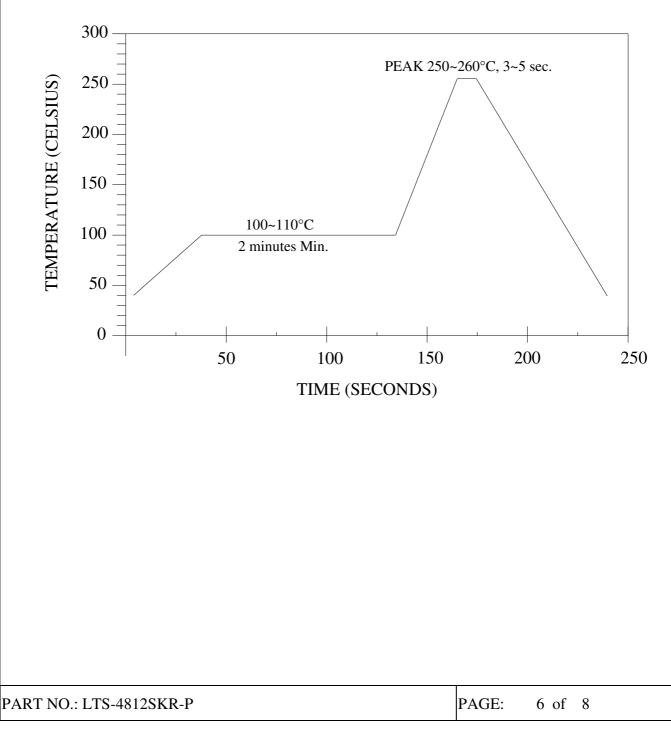
PART NO.: LTS-4812SKR-P	PAGE:	4 of 8	8
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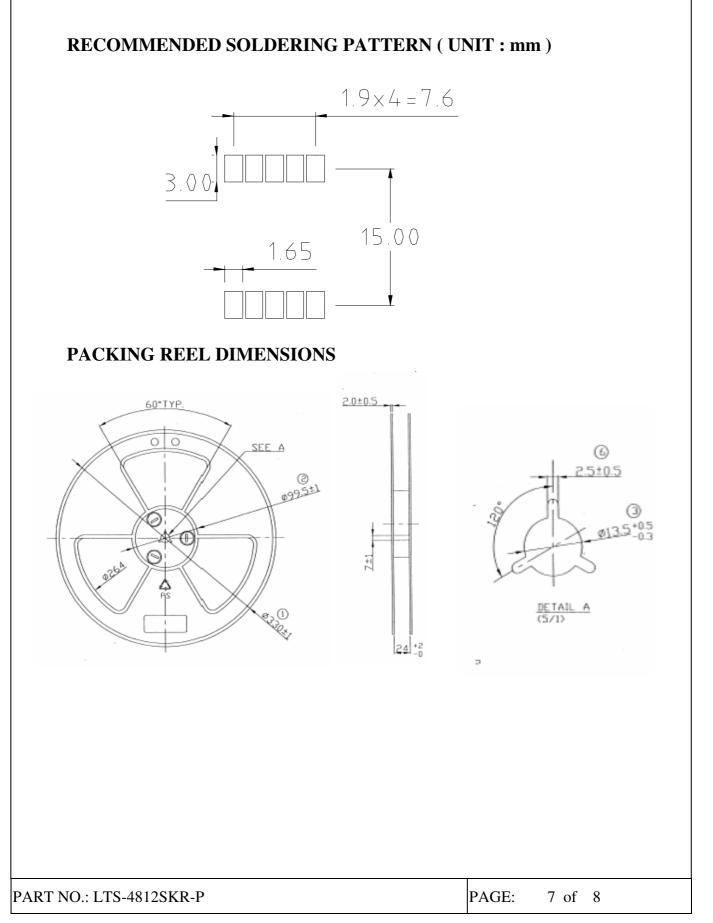
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SMT SOLDERING INSTRUCTION

(Number of reflow process shall be less than 2 times, and cooling process to normal temperature is required between the first and the second soldering process)

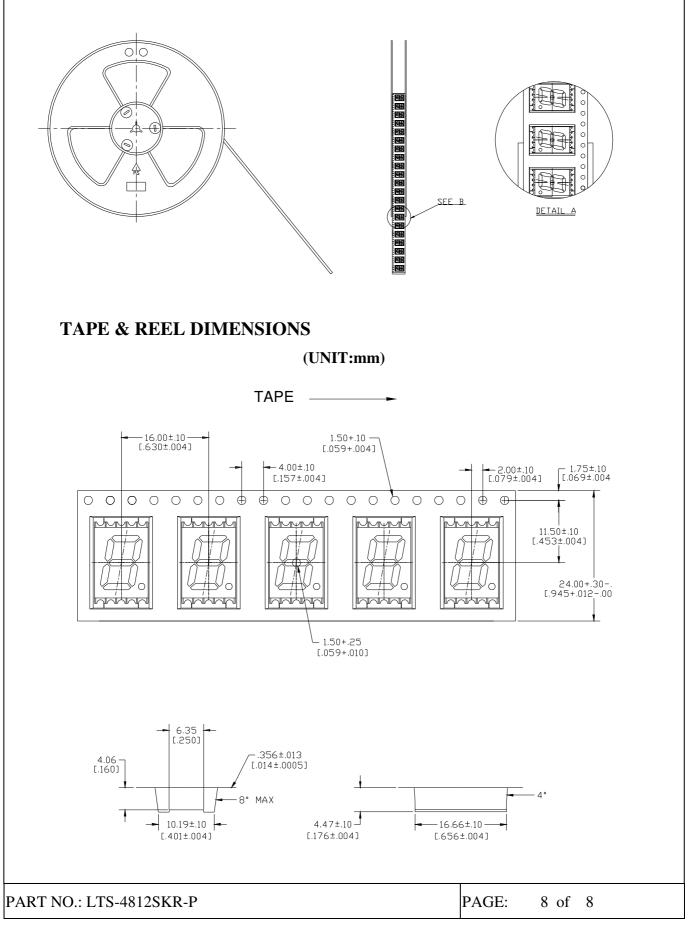


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