



TS494

Voltage Mode PWM Controller



Supply Voltage Range 7 V to 40V

Output Driving Current 200mA

Oscillator Frequency up to 200KHz

General Description

The TS494 incorporates on a single monolithic chip all the functions required in the construction of a pulse-width-modulation control circuit. Designed primarily for power supply control, these devices offer the systems engineer the flexibility to tailor the power supply control circuitry to his application.

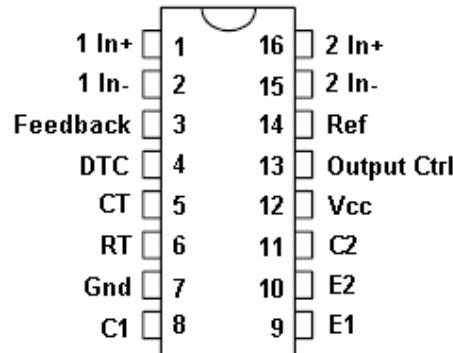
The TS494 contains an error amplifier, an on-chip adjustable oscillator, a dead-time control comparator, pulse-steering control flip-flop, a 5-volt, 5% precision regulator, and output-control circuits. The error amplifier exhibits a common-mode voltage from -0.3V to Vcc -2V. The dead-time control comparator has a fixed offset that provides approximately 5% dead time when externally altered. The on-chip oscillatory be bypassed by terminating R_T (pin 6) to the reference output and providing a sawtooth input to C_T (pin 5), or it may be used to drive the common circuits in synchronous multiple-rail power supplies. The uncommitted output transistor provide either common-emitter or emitter-follower output capability. Each device provides for push-pull or single-ended output operation, which may be selected through the output-control function. The architecture of these devices prohibits the possibility of either output being pulsed twice during push-pull operation.

The TS494 is offered in SOP-16 and DIP-16 package.

Features

- ❖ Complete PWM Power Control Circuitry
- ❖ Uncommitted Outputs for 200mA Sink or Source Current
- ❖ Output Control Selects Single-Ended or Push –Pull Operation
- ❖ Internal Circuitry Prohibits Double Pulse at Either Output
- ❖ Variable Dead-Time Provides Control over Total Range
- ❖ Internal Regulator Provides a Stable 5-V Reference Supply with 5% Tolerance
- ❖ Circuit Architecture Allows Easy Synchronization

Pin Assignment



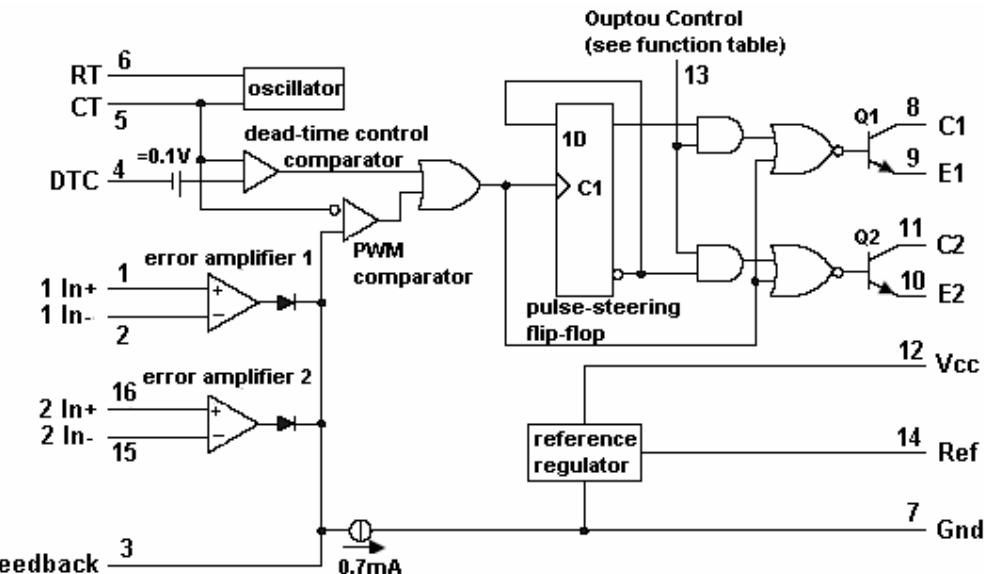
Applications

- ❖ Charger
- ❖ DC to DC converters

Ordering Information

Part No.	Operating Temp.	Package
TS494CD	0 ~ +70 °C	DIP-16
TS494CS		SOP-16

Function Block Diagram



Absolute Maximum Rating

Rating	Symbol	Value	Unit
Power Supply Voltage	Vcc	40	V
Collector Output Voltage	Vc1, Vc2	40	V
Collector Output Current (each transistor)	Ic1, Ic2	250	mA
Amplifier Input Voltage Range	V _{IR}	- 30 ~ +40	V
Power Dissipation @ Ta<=45 °C	Pd	1	W
Thermal Resistance, Junction to Ambient	R _{θJA}	80	°C/W
Operating Junction Temperature	T _j	125	°C
Storage Temperature Range	T _{stg}	-55 ~ +125	°C
Operating Ambient Temperature Range	T _a	0 ~ +70	°C
Derating Ambient Temperature	T _a	45	°C

Recommended Operating Conditions

Rating	Symbol	Min.	Typ.	Max	Unit
Power Supply Voltage	Vcc	7	15	40	V
Collector Output Voltage	Vc1, Vc2	--	30	40	V
Collector Output Current (each transistor)	Ic1, Ic2	--	--	200	mA
Amplifier Input Voltage	V _{in}	- 0.3	--	Vcc-2.0	V
Current Into Feedback Terminal	I _{fb}	--	--	0.3	mA
Reference Output Current	I _{ref}	--	--	10	mA
Timing Resistor	R _T	1.8	30	500	kΩ
Timing Capacitor	C _T	0.0047	0.001	10	μF
Oscillator Frequency	Fosc	1	40	200	KHz



Electrical Characteristics (Ta=25°C, V_{CC} = 15V, C_T= 0.01μF, R_T= 12kΩ; unless otherwise specified.)

REFERENCE SECTION

Parameter	Test Conditions*	Min	Typ	Max	Unit
Output voltage (V _{ref})	I _O =1mA	4.75	5	5.25	V
Line regulation	V _{CC} = 7V to 40V	--	2.0	25	mV
Load regulation	I _O = 1mA to 10mA	--	3.0	15	
Output voltage change with temp.	ΔT _A =Min to Max	--	0.2	1	%
Short-circuit output current***	V _{ref} = 0	15	35	75	mA

OSCILLATOR SECTION

Frequency	C _T = 0.001μF, R _T = 30kΩ	--	40	--	KHz
Standard deviation of frequency****	C _T = 0.001μF, R _T = 30kΩ	--	3.0	--	%
Frequency change with voltage	V _{CC} = 7V to 40V	--	0.1	--	
Frequency change with temperature***	C _T = 0.001μF, R _T = 30kΩ ΔT _A = Min to Max	--	--	12	

ERROR AMPLIFIER SECTION

Input offset voltage	V _O = (pin 3)= 2.5V	--	2.0	10	mV
Input offset current	V _O = (pin 3)= 2.5V	--	5.0	250	nA
Input bias current	V _O = (pin 3)= 2.5V	--	-0.1	-1.0	μA
Common-mode input voltage range	V _{CC} = 7V to 40V	-0.3 to V _{CC} -2			V
Open-loop voltage amplification	ΔV _O = 3V, R _L =2kΩ, V _O = 0.5 to 3.5V	70	95	--	dB
Unity-gain bandwidth	R _L = 2kΩ, V _O = 0.5 to 3.5V	--	800	--	KHz
Common-mode rejection ratio	ΔV _O = 40V	65	90	--	dB
Power Supply Rejection Ratio	ΔV _{CC} = 33V, V _O = 2.5V, R _L = 2kΩ	--	100	--	dB
Output sink current (pin 3)	V _{ID} = -15mV to -5V, V(pin3)= 0.7V	0.3	0.7	--	mA
Output source current (pin 3)	V _{ID} = 15mV to 5V, V(pin3)= 3.5V	2.0	-4.0	--	mA

OUTPUT SECTION

Collector off-state current	V _{CE} = 40V, V _{CC} = 40V	--	2.0	100	μA
Emitter off-state current	V _{CC} = 40V, V _E = 0	--	--	-100	
Collector-emitter saturation voltage	Common-emitter V _E = 0, I _C = 200mA	--	1.1	1.3	V
	Emitter-follower V _C = 15V, I _E = -200mA	--	1.5	2.5	
Output control input current	V _I = V _{ref}	--	--	3.5	mA

DEAD-TIME CONTROL SECTION

Input bias current (pin 4)	V _I = 0 to 5.25V	--	-2.0	-10	μA
Maximum duty cycle, each output	V _I (pin 4)= 0, C _T = 0.1μF, R _T = 12kΩ	--	45	50	%
Input threshold voltage(pin 4)	Zero duty cycle	--	3.0	3.3	
	Maximum duty cycle	0	--	--	V



Electrical Characteristics (continues)

PWM COMPARATOR SECTION

Parameter	Test Conditions*	Min	Typ	Max	Unit
Input threshold voltage (pin 3)	Zero duty cycle	--	4.0	4.5	V
Input sink current (pin 3)	V (pin 3)=0.7V	0.3	0.7	--	mA

TOTAL DEVICE

Standby supply current	pin 6 at Vref, all other inputs and outputs open	Vcc=15V	--	6.0	10	mA
		Vcc=40V	--	9.0	15	
Average supply current	Vi (pin4)= 2V, See Figure 1		--	7.5	--	

SWITCHING CHARACTERISTICS

Output voltage rise time	Common-emitter configuration, See Figure 3	--	100	200	ns
Output voltage fall time		--	25	100	
Output voltage rise time	Emitter-follower configuration, See Figure 4	--	100	200	
Output voltage fall time		--	40	100	

UNDERVOLTAGE LOCKOUT SECTION

Turn-on Threshold	Vcc increasing Iref = 1.0mA	5.5	6.43	7.0	V
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Remark:

- All typical value except for temperature coefficient are at Ta= 25 °C
- For conditions shown as Min or Max, use the appropriate value specified under recommended operating conditions.
- All typical values except for parameter changes with temperature are at Ta= 25 °C
- Duration of the short-circuit should not exceed one second
- Standard deviation is a measure of the statistical distribution about the mean as derived from the formula

$$\sigma = \left[\sum_{n=1}^N \frac{(x_n - \bar{x})^2}{N-1} \right]^{1/2}$$

- Temperature coefficient of timing capacitor and timing resistor not taken into account

Parameter Measurement Information

Parameter Measurement Information

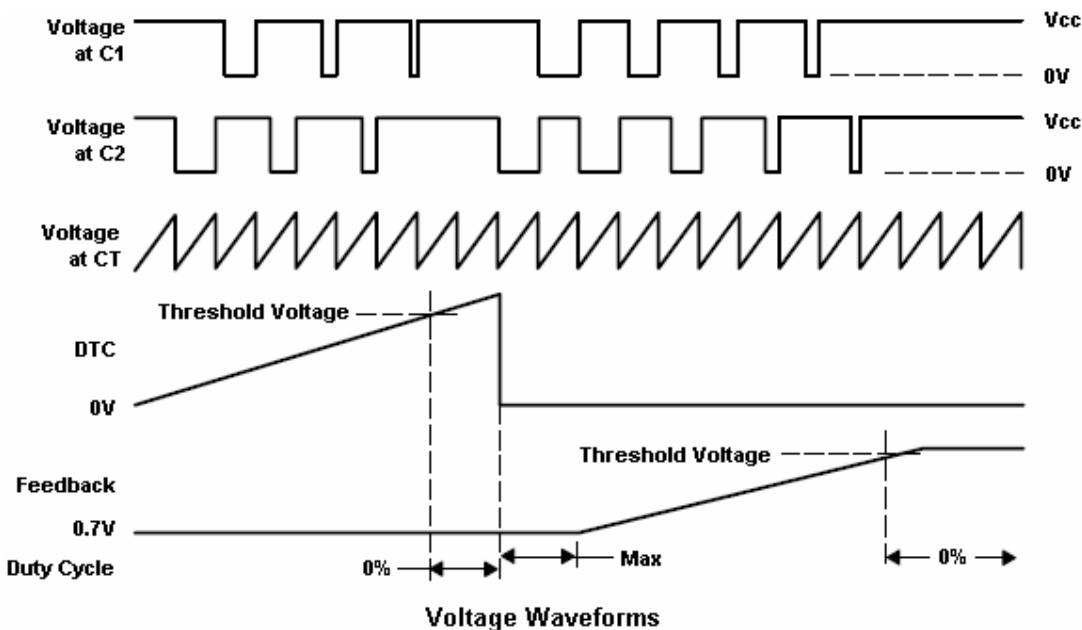
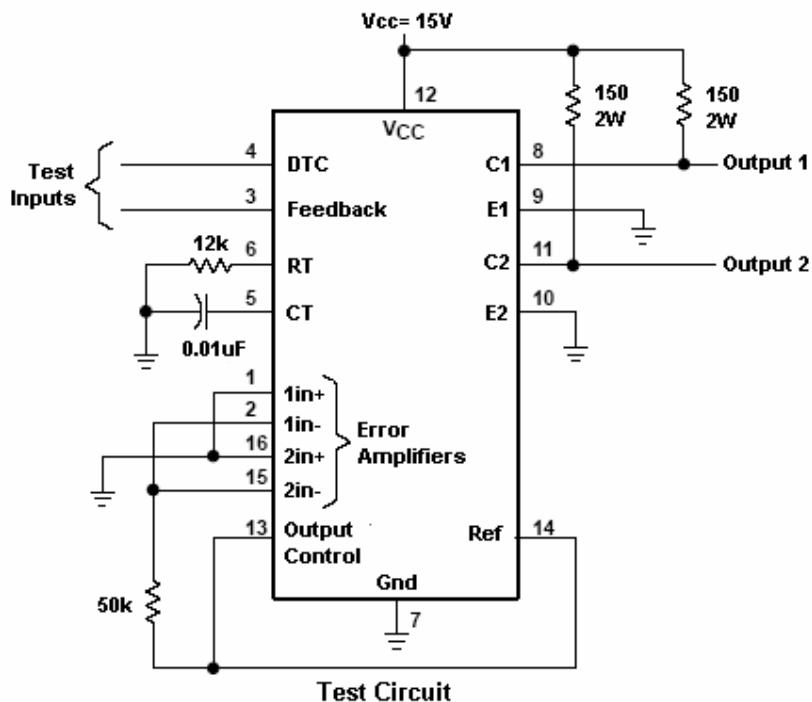


Fig 1. Operational Test Circuit and Wave Forms

Parameter Measurement Information (continues)

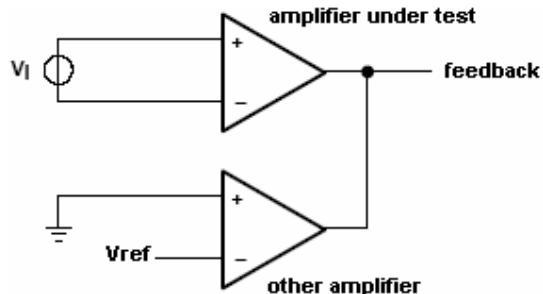
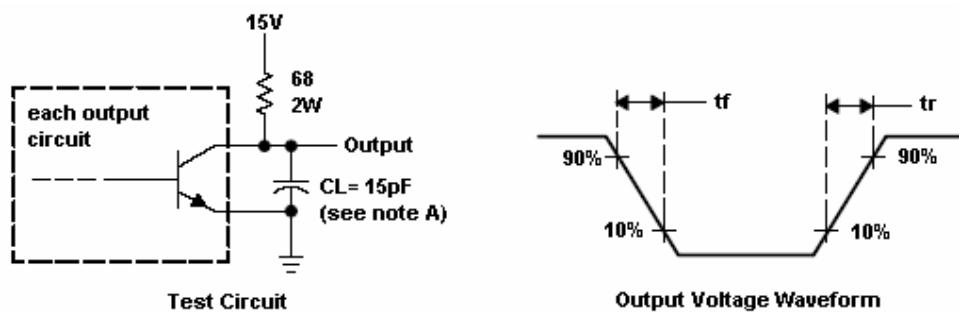
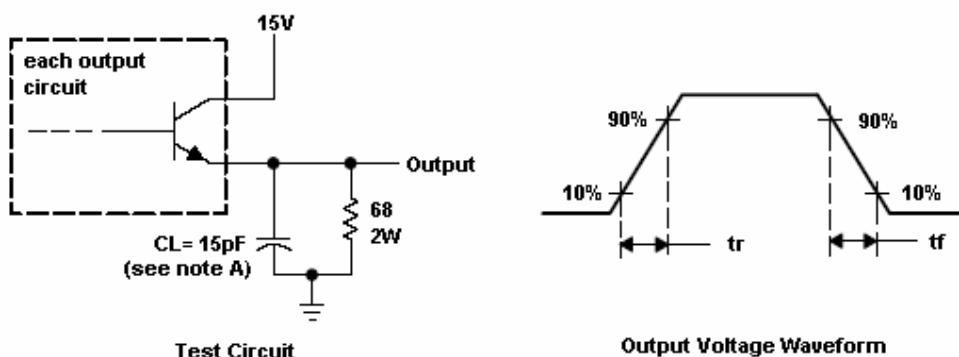


Fig 2. Amplifier Characteristics



note A: CL includes probe and jig capacitance

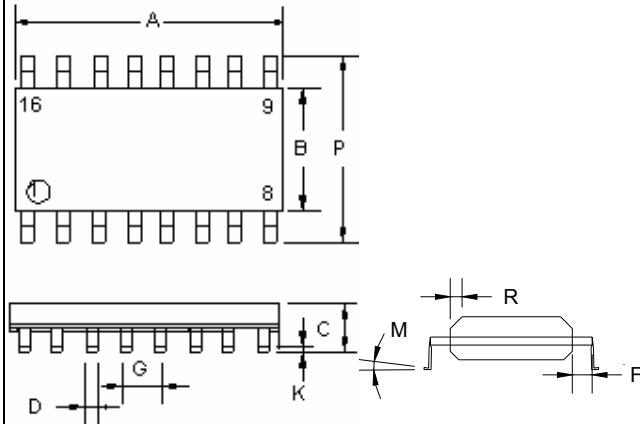
Fig 3. Common-Emitter Configuration



note A: CL includes probe and jig capacitance

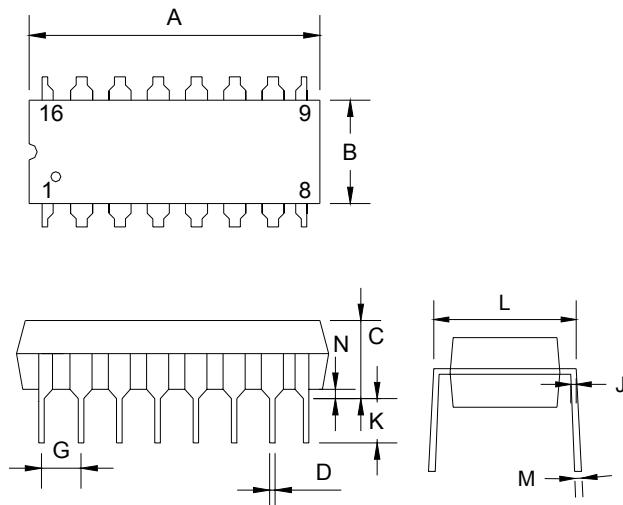
Fig 4. Emitter-Follower Configuration

SOP-16 Mechanical Drawing



DIM	MILLIMETERS		INCHES	
	MIN	MAX	MIN	MAX
A	9.80	10.00	0.386	0.393
B	3.80	4.00	0.150	0.157
C	1.35	1.75	0.054	0.068
D	0.35	0.49	0.014	0.019
F	0.40	1.25	0.016	0.049
G	1.27 (typ)		0.05 (typ)	
K	0.10	0.25	0.004	0.009
M	0°	7°	0°	7°
P	5.80	6.20	0.229	0.244
R	0.25	0.50	0.010	0.019

DIP-16 Mechanical Drawing



DIM	MILLIMETERS		INCHES	
	MIN	MAX	MIN	MAX
A	17.80	18.05	0.701	0.710
B	6.22	6.48	0.290	0.299
C	3.18	4.45	0.125	0.135
D	0.35	0.55	0.019	0.020
G	1.27 (typ)		0.05 (typ)	
J	0.25	0.31	0.010	0.012
K	3.25	3.35	0.128	0.132
L	7.75	8.00	0.305	0.315
M	-	10°	-	10°