

SOT-23

## Pin Definition:

1. Gate 2. Source 3. Drain

### **PRODUCT SUMMARY**

V <sub>DS</sub> (V)	R <sub>DS(on)</sub> (mΩ)	I <sub>D</sub> (A)
	70 @ V <sub>GS</sub> = -4.5V	-3.3
-20	90 @ V <sub>GS</sub> = -2.5V	-2.0
	130 @ V <sub>GS</sub> = -1.8V	-1.0

### Features

- Advance Trench Process Technology
- High Density Cell Design for Ultra Low On-resistance

### **Application**

- Load Switch
- PA Switch

## **Ordering Information**

Part No.	Package	Packing
TSM2313CX RF	SOT-23	T&R

#### Absolute Maximum Rating (Ta = 25 °C unless otherwise noted)

Parameter		Symbol	Limit	Unit
Drain-Source Voltage		V <sub>DS</sub>	-20V	V
Gate-Source Voltage		V <sub>GS</sub>	±12	V
Continuous Drain Current, V <sub>GS</sub> @4.5	<i>I</i> .	I <sub>D</sub>	-3.3	А
Pulsed Drain Current, V <sub>GS</sub> @4.5V		I <sub>DM</sub>	-20	А
Continuous Source Current (Diode Conduction) <sup>a,b</sup>		Is	-1.7	А
Meximum Dever Dissinction	Ta = 25 °C		2	10/
Maximum Power Dissipation	Ta = 70°C	PD	1.3	W
Operating Junction Temperature		TJ	+150	°C
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>STG</sub>	- 55 to +150	°C

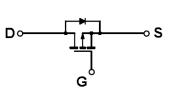
### **Thermal Performance**

Parameter	Symbol	Limit	Unit
Junction to Foot (Drain) Thermal Resistance	RƏ <sub>JF</sub>	30	°C/W
Junction to Ambient Thermal Resistance (PCB mounted)	RƏ <sub>JA</sub>	50	°C/W

Notes:

a. Pulse width limited by the Maximum junction temperature

b. Surface Mounted on FR4 Board, t  $\leq$  5 sec.



P-Channel MOSFET

**Block Diagram** 



#### **Electrical Specifications**

NCE

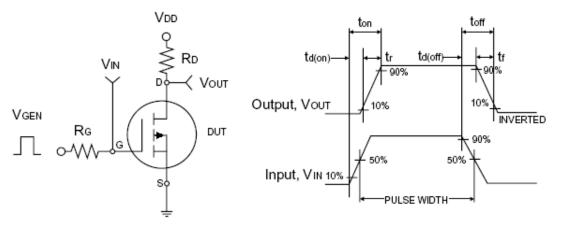
COMF

Parameter	Conditions Symbo		Min	Тур	Max	Unit
Static				•	•	
Drain-Source Breakdown Voltage	$V_{GS} = 0V, I_{D} = -250uA$	BV <sub>DSS</sub>	-20			V
Gate Threshold Voltage	$V_{DS} = V_{GS}, I_{D} = -250 \mu A$	V <sub>GS(TH)</sub>	-0.6		-1.4	V
Gate Body Leakage	$V_{GS} = \pm 12V, V_{DS} = 0V$	I <sub>GSS</sub>			±100	nA
Zero Gate Voltage Drain Current	$V_{DS}$ = -20V, $V_{GS}$ = 0V	I <sub>DSS</sub>			-1.0	μA
On-State Drain Current <sup>a</sup>	$V_{DS}$ =-5V, $V_{GS}$ = -4.5V	I <sub>D(ON)</sub>	-15			Α
	$V_{GS}$ = -4.5V, $I_{D}$ = -3.3A			55	70	mΩ
Drain-Source On-State Resistance <sup>a</sup>	$V_{GS}$ = -2.5V, $I_{D}$ = -2.0A	R <sub>DS(ON)</sub>		70	90	
	$V_{GS}$ = -1.8V, $I_{D}$ = -1.0A			120	130	
Forward Transconductance <sup>a</sup>	$V_{DS}$ = -10V, $I_{D}$ = -4.7A	<b>g</b> <sub>fs</sub>		11		S
Diode Forward Voltage	I <sub>S</sub> = -1.7A, V <sub>GS</sub> = 0V	V <sub>SD</sub>		-0.8	-1.2	V
Dynamic <sup>b</sup>	·					
Total Gate Charge		Qg		6	9	
Gate-Source Charge	$V_{DS} = -10V, I_D = -4.7A,$	Q <sub>gs</sub>		1.4		nC
Gate-Drain Charge	$V_{GS} = -4.5V$	Q <sub>gd</sub>		1.9		
Input Capacitance		C <sub>iss</sub>		640		
Output Capacitance	$V_{DS} = -10V, V_{GS} = 0V,$	C <sub>oss</sub>		180		pF
Reverse Transfer Capacitance	f = 1.0MHz	C <sub>rss</sub>		90		
Switching <sup>c</sup>						
Turn-On Delay Time		t <sub>d(on)</sub>		22	35	
Turn-On Rise Time	$V_{DD} = -10V, R_L = 10\Omega,$	t <sub>r</sub>		35	55	
Turn-Off Delay Time	$I_{\rm D}$ = -1A, $V_{\rm GEN}$ = -4.5V,	t <sub>d(off)</sub>		45	70	nS
Turn-Off Fall Time	$R_G = 6\Omega$	t <sub>f</sub>		25	50	

Notes:

a. pulse test: PW ≤300µS, duty cycle ≤2% b. For DESIGN AID ONLY, not subject to production testing.

b. Switching time is essentially independent of operating temperature.

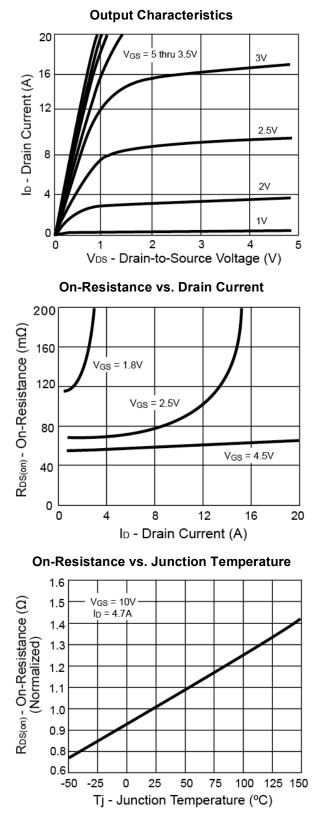


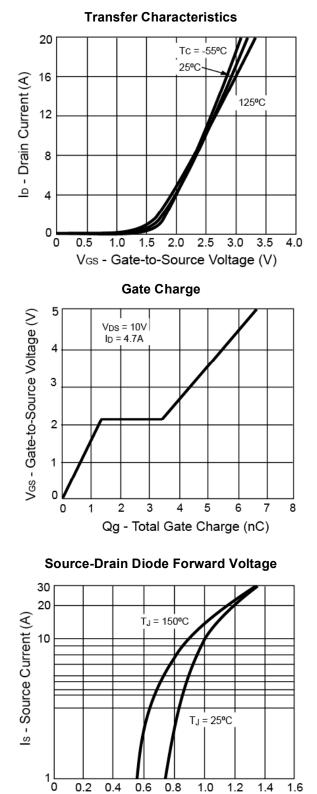
Switching Test Circuit

Switchin Waveforms



#### Electrical Characteristics Curve (Ta = 25 °C, unless otherwise noted)

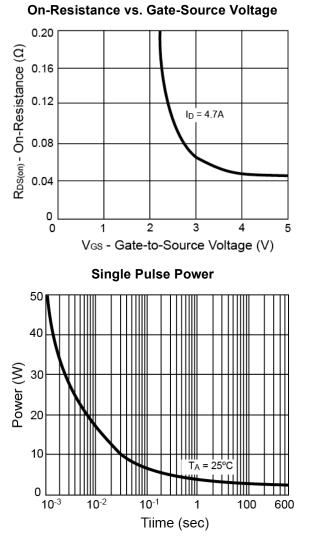


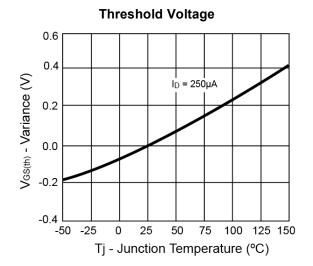


Vsp - Source-to-Drain Voltage (V)

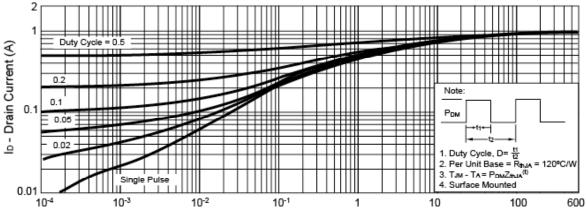


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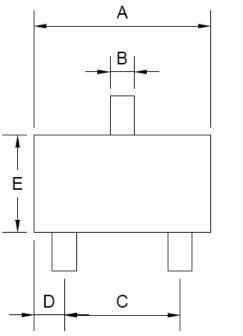
#### Normalized Thermal Transient Impedance, Junction-to-Ambient



Square Wave Pulse Duration (sec)



# SOT-23 Mechanical Drawing



F	_	
_		G

SOT-23 DIMENSION					
DIM	MILLIMETERS		INCHES		
DIN	MIN	MAX	MIN	MAX.	
A	2.88	2.91	0.113	0.115	
В	0.39	0.42	0.015	0.017	
С	1.78	2.03	0.070	0.080	
D	0.51	0.61	0.020	0.024	
E	1.59	1.66	0.063	0.065	
F	1.04	1.08	0.041	0.043	
G	0.07	0.09	0.003	0.004	



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