

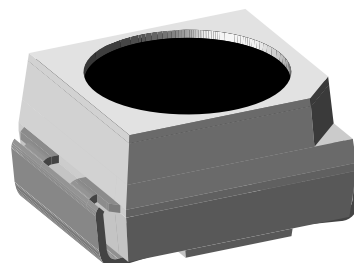
Silicon NPN Phototransistor

Description

TEMT3700F is a high speed silicon NPN epitaxial planar phototransistor in a miniature PLCC-2 package with integrated IR band pass filter (950 nm).

Features

- High sensitivity
- IR Filter (950 nm band pass)
- Suitable for near infrared radiation
- Extra wide viewing angle $\varphi = \pm 60^\circ$
- Fast response times
- PLCC-2 SMD package
- Package notch = collector
- No base terminal
- Matched to IR emitter TSMS3700 and TSML3710
- Lead (Pb)-free component
- Component in accordance to RoHS 2002/95/EC and WEEE 2002/96/EC



19032

Applications

- Touch sensors
- Transmissive sensors
- Reflective sensors

Parts Table

Part	Ordering code	Remarks
TEMT3700F-GS08	TEMT3700F-GS08	MOQ: 7500 pc (5 reels)
TEMT3700F-GS18	TEMT3700F-GS18	MOQ: 8000 pc (1 reel)

Absolute Maximum Ratings

$T_{amb} = 25^\circ\text{C}$, unless otherwise specified

Parameter	Test condition	Symbol	Value	Unit
Collector emitter voltage		V_{CEO}	70	V
Emitter collector voltage		V_{ECO}	5	V
Collector current		I_C	50	mA
Collector peak current	$t_p/T \leq 0.1$, $t_p \leq 10 \mu\text{s}$	I_{CM}	100	mA
Total power dissipation	$T_{amb} \leq 55^\circ\text{C}$	P_{tot}	100	mW
Junction temperature		T_j	100	$^\circ\text{C}$
Storage temperature range		T_{stg}	- 40 to + 100	$^\circ\text{C}$
Soldering temperature	$t \leq 3 \text{ s}$	T_{sd}	260	$^\circ\text{C}$
Thermal resistance junction/ambient		R_{thJA}	450	K/W

Electrical Characteristics

$T_{amb} = 25\text{ }^{\circ}\text{C}$, unless otherwise specified

Parameter	Test condition	Symbol	Min	Typ.	Max	Unit
Collector emitter breakdown voltage	$I_C = 1\text{ mA}$	$V_{(BR)CEO}$	70			V
Collector-emitter dark current	$V_{CE} = 20\text{ V}$, $E = 0$	I_{CEO}		1	200	nA
Collector-emitter capacitance	$V_{CE} = 5\text{ V}$, $f = 1\text{ MHz}$, $E = 0$	C_{CEO}		3		pF

Optical Characteristics

$T_{amb} = 25\text{ }^{\circ}\text{C}$, unless otherwise specified

Parameter	Test condition	Symbol	Min	Typ.	Max	Unit
Collector light current	$E_e = 1\text{ mW/cm}^2$, $\lambda = 950\text{ nm}$, $V_{CE} = 5\text{ V}$	I_{ca}	0.25	0.5		mA
Angle of half sensitivity		ϕ		± 60		deg
Wavelength of peak sensitivity		λ_p		940		nm
Range of spectral bandwidth		$\lambda_{0.5}$		860 to 1050		nm
Collector emitter saturation voltage	$E_e = 1\text{ mW/cm}^2$, $\lambda = 950\text{ nm}$, $I_C = 0.1\text{ mA}$	V_{CEsat}		0.15	0.3	V
Rise time / Fall time	$V_S = 5\text{ V}$, $I_C = 1\text{ mA}$, $\lambda = 950\text{ nm}$, $R_L = 1\text{ k}\Omega$	t_r / t_f		6		μs
	$V_S = 5\text{ V}$, $I_C = 1\text{ mA}$, $\lambda = 950\text{ nm}$, $R_L = 100\text{ }\Omega$	t_r / t_f		2		μs
Cut-Off frequency	$V_S = 5\text{ V}$, $I_C = 2\text{ mA}$, $R_L = 100\text{ }\Omega$	f_c		180		kHz

Typical Characteristics

$T_{amb} = 25\text{ }^{\circ}\text{C}$, unless otherwise specified

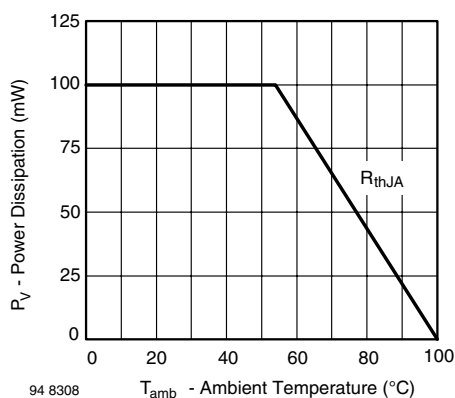


Figure 1. Total Power Dissipation vs. Ambient Temperature

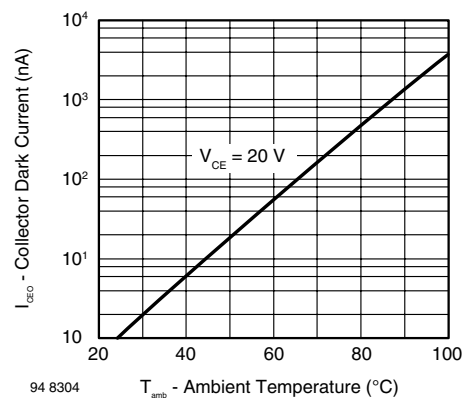


Figure 2. Collector Dark Current vs. Ambient Temperature

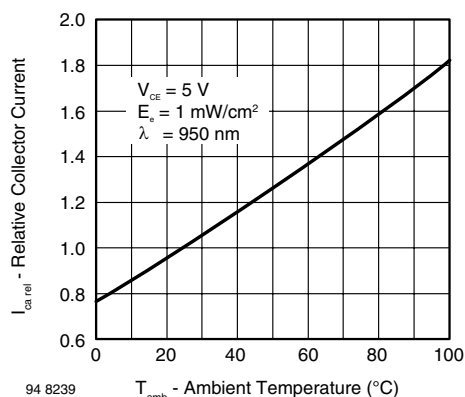


Figure 3. Relative Collector Current vs. Ambient Temperature

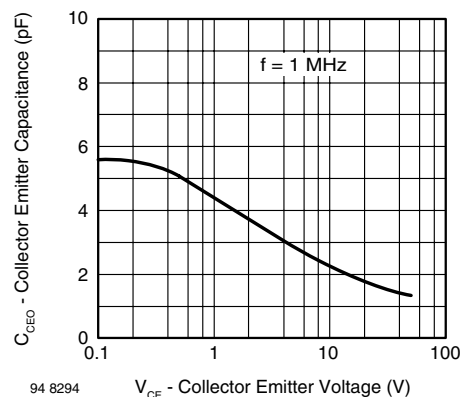


Figure 6. Collector Emitter Capacitance vs. Collector Emitter Voltage

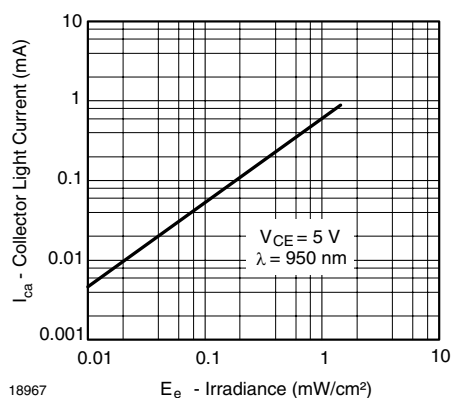


Figure 4. Collector Light Current vs. Irradiance

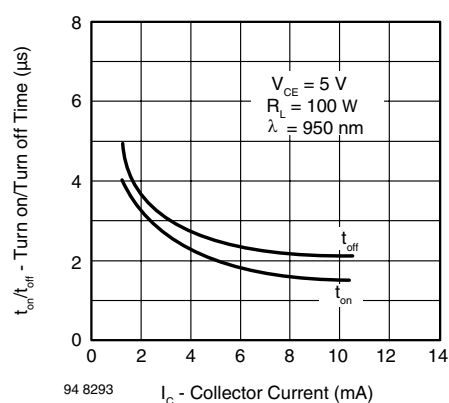


Figure 7. Turn On/Turn Off Time vs. Collector Current

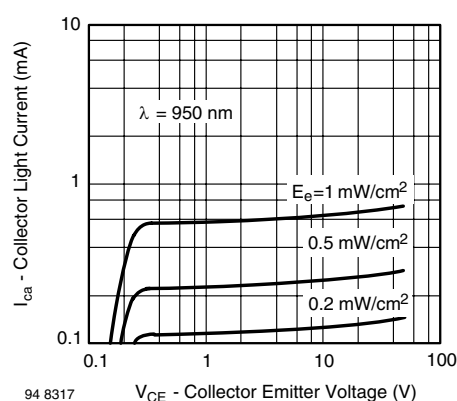


Figure 5. Collector Light Current vs. Collector Emitter Voltage

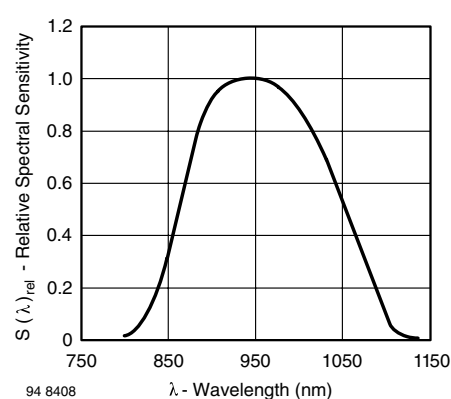


Figure 8. Relative Spectral Sensitivity vs. Wavelength

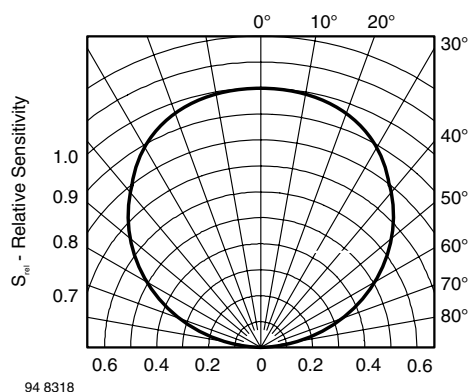
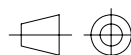
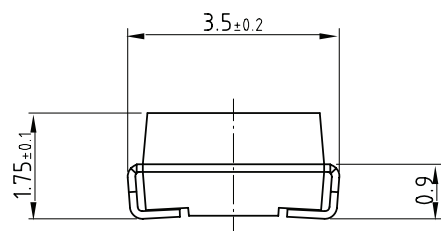
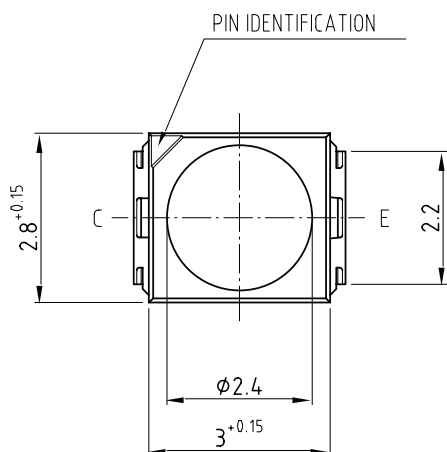


Figure 9. Relative Radiant Sensitivity vs. Angular Displacement

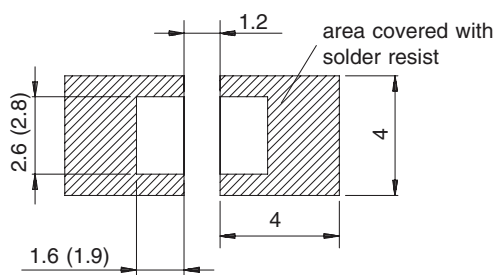
Package Dimensions in mm



technical drawings
according to DIN
specifications



Mounting Pad Layout



Drawing-No.: 6.541-5025.03-4

Issue: 5; 21.11.05

20350

Temperature - Time Profile

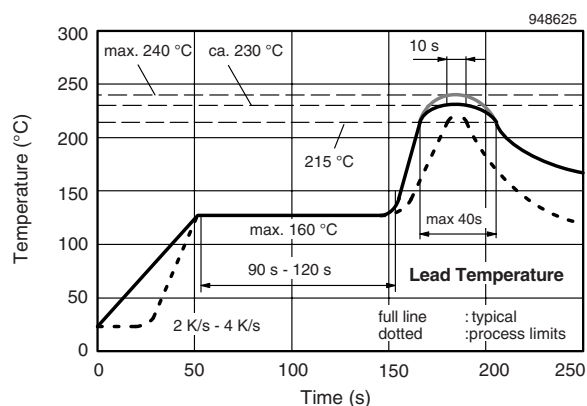


Figure 10. Lead Tin (SnPb) Reflow Solder Profile

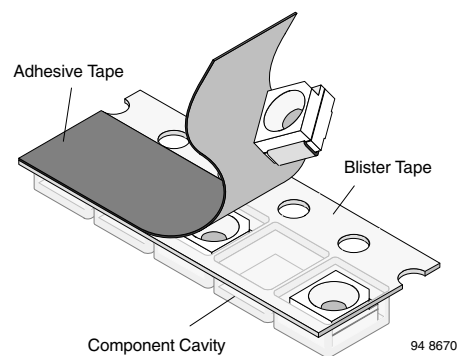


Figure 11. Blister Tape

Drypack

Devices are packed in moisture barrier bags (MBB) to prevent the products from moisture absorption during transportation and storage. Each bag contains a desiccant.

Floor Life

Floor life (time between soldering and removing from MBB) must not exceed the time indicated in J-STD-020. TEMT3700F is released for: Moisture Sensitivity Level 2, according to JEDEC, J-STD-020

Floor Life: 1 year

Conditions: $T_{amb} < 30\text{ °C}$, $RH < 60\%$

Drying

In case of moisture absorption devices should be baked before soldering. Conditions see J-STD-020 or Label. Devices taped on reel dry using recommended conditions 192 h at 40 °C (+ 5 °C), $RH < 5\%$

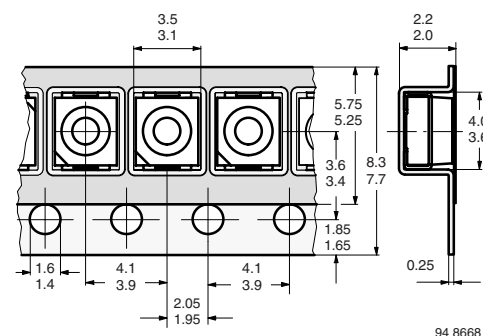


Figure 12. Tape Dimensions in mm for PLCC-2

Missing Devices

A maximum of 0.5 % of the total number of components per reel may be missing, exclusively missing components at the beginning and at the end of the reel. A maximum of three consecutive components may be missing, provided this gap is followed by six consecutive components.

Cover Tape Removal Force

The removal force lies between 0.1 N and 1.0 N at a removal speed of 5 mm/s. In order to prevent components from popping out of the blisters, the cover tape must be pulled off at an angle of 180° with regard to the feed direction.

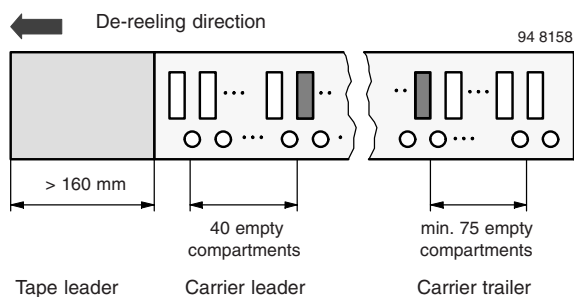


Figure 13. Beginning and End of Reel

The tape leader is at least 160 mm and is followed by a carrier tape leader with at least 40 empty compartments. The tape leader may include the carrier tape as long as the cover tape is not connected to the carrier tape. The least component is followed by a carrier tape trailer with a least 75 empty compartments and sealed with cover tape.

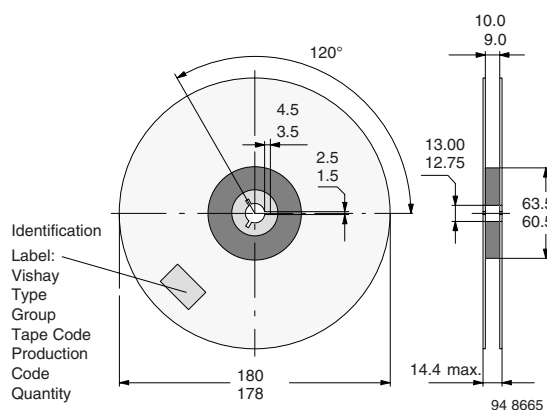


Figure 14. Dimensions of Reel



Ozone Depleting Substances Policy Statement

It is the policy of Vishay Semiconductor GmbH to

1. Meet all present and future national and international statutory requirements.
2. Regularly and continuously improve the performance of our products, processes, distribution and operating systems with respect to their impact on the health and safety of our employees and the public, as well as their impact on the environment.

It is particular concern to control or eliminate releases of those substances into the atmosphere which are known as ozone depleting substances (ODSs).

The Montreal Protocol (1987) and its London Amendments (1990) intend to severely restrict the use of ODSs and forbid their use within the next ten years. Various national and international initiatives are pressing for an earlier ban on these substances.

Vishay Semiconductor GmbH has been able to use its policy of continuous improvements to eliminate the use of ODSs listed in the following documents.

1. Annex A, B and list of transitional substances of the Montreal Protocol and the London Amendments respectively
2. Class I and II ozone depleting substances in the Clean Air Act Amendments of 1990 by the Environmental Protection Agency (EPA) in the USA
3. Council Decision 88/540/EEC and 91/690/EEC Annex A, B and C (transitional substances) respectively.

Vishay Semiconductor GmbH can certify that our semiconductors are not manufactured with ozone depleting substances and do not contain such substances.

We reserve the right to make changes to improve technical design
and may do so without further notice.

Parameters can vary in different applications. All operating parameters must be validated for each customer application by the customer. Should the buyer use Vishay Semiconductors products for any unintended or unauthorized application, the buyer shall indemnify Vishay Semiconductors against all claims, costs, damages, and expenses, arising out of, directly or indirectly, any claim of personal damage, injury or death associated with such unintended or unauthorized use.

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